

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of the Claims:

1-16 (Cancelled)

17. (Previously Presented) A method, comprising:

activating an idle storage device in a computer system to transfer data while a main processor of the computer is idle;

executing the data transfer; and

returning system resources to an idle state.

18. (Previously Presented) The method of claim 17, further comprising:

buffering the data for transfer.

19. (Previously Presented) The method of claim 17, further comprising:

detecting a request for data transfer to activate the idle storage device while the main processor of the computer is idle.

20. (Previously Presented) The method of claim 19, wherein a controller activates the idle storage device by directing power to the device.

21. (Previously Presented) The method of claim 17, further comprising:
tagging the transferred data for recognition.
22. (Previously Presented) The method of claim 17, further comprising:
apportioning a system time and power resource based on the transferred data.
23. (Previously Presented) The method of claim 22, further comprising:
returning the system resource to a pre-transfer state.
24. (Previously Presented) The method of claim 17, further comprising:
notifying a user of the computer system of the data transfer after the system is
returned to an idle state.
25. (Previously Presented) The method of claim 17, wherein the data is transferred
wirelessly.
26. (Previously Presented) The method of claim 17, wherein the data is transferred
via a low level data bus.
27. (Previously Presented) An apparatus comprising:
means for activating an idle storage device in a computer system to transfer data
while a main processor of the computer is idle;
means for executing the data transfer; and

means for returning system resources to an idle state.

28. (Previously Presented) The apparatus of claim 27, further comprising:

means for buffering the data for transfer.

29. (Previously Presented) The apparatus of claim 27, wherein the means for activating the idle storage device comprise a controller that detects a request for data transfer while the main processor of the computer is idle.

30. (Previously Presented) The apparatus of claim 29, wherein the controller activates the idle storage device by directing power to the device.

31. (Previously Presented) The apparatus of claim 27, wherein the data is transferred wirelessly.

32. (Previously Presented) The apparatus of claim 27, wherein the data is transferred via a low level data bus.

33. (Previously Presented) A machine-readable medium having executable instructions to cause a processor to perform a method, the method comprising:

activating an idle storage device in a computer system to transfer data while a main processor of the computer is idle;

executing the data transfer; and

returning system resources to an idle state.

34. (Previously Presented) The machine-readable medium of claim 33, wherein the method further comprises:

buffering the data for transfer.

35. (Previously Presented) The machine-readable medium of claim 34, wherein the idle storage device is activated by a controller that detects a request for data transfer while the main processor of the computer is idle.

36. (Previously Presented) The machine-readable medium of claim 33, wherein the method further comprises:

apportioning a system resource based on the transferred data.

37. (Previously Presented) The machine-readable medium of claim 36, wherein the method further comprises:

returning the system resource to a pre-transfer state.

38. (Previously Presented) A computer system comprising:

a processor coupled to a memory through a bus;

a unit to activate a storage device in a computer system to transfer data while the processor is idle, the unit to

execute the data transfer, and the unit to

return system resources to an idle state.

39. (Previously Presented) The system of claim 38, further including a buffer to store data to be transferred.

40. (Previously Presented) The system of claim 38, further including a unit to detect a request for data transfer to activate the idle storage device while the main processor of the computer is idle.